

**AMENDMENTS TO THE SPECIFICATION**

**Please replace fourth full paragraph bridging pages 6-7 with the following rewritten paragraph:**

In Fig. 3, which is a block circuit diagram of a second driving apparatus of Figs. 1A, 1B, 1C and 1D, the main LCD unit 105 and the sub LCD unit 107 are both of a TFT-type. In this case, the scan lines of the sub LCD unit 107 are short-circuited to the corresponding scan lines of the main LCD unit 104, so that the ~~scan-row~~ driver circuit 204 of Fig. 2 is omitted. Note that, if the data lines of the sub LCD unit 107 are short-circuited to the corresponding data lines of the main LCD unit 105, the ~~data-column~~ driver circuit 203 of Fig. 2 can be omitted.

**Please replace fourth full paragraph bridging pages 7-8 with the following rewritten paragraph:**

A data driver circuit 3 drives the 144 data lines of the main LCD unit 1 and the 64 data lines of the sub LCD unit 2. On the other hand, a scan driver circuit ~~3~~ 4 drives the 176 scan lines of the main LCD unit 1 and the 96 scan lines of the sub LCD unit 2.

**Please replace the fifth paragraph on page 13 with the following rewritten paragraph:**

A level shift circuit 403 performs a level shift operation upon the output signals of the logic circuit 402 and transmits level-shifted output signals data to an output circuit 404. Note that the shift register circuit 401 and the logic circuit 402 are usually operated under a power supply voltage of 3V, while the output circuit 404 is operated under a power supply voltage of 4 to 5V. Therefore, a voltage level shift is required. However, if all the circuits are operated under the same power supply voltage, the level shift circuit 403 is unnecessary.